

FIG. 1

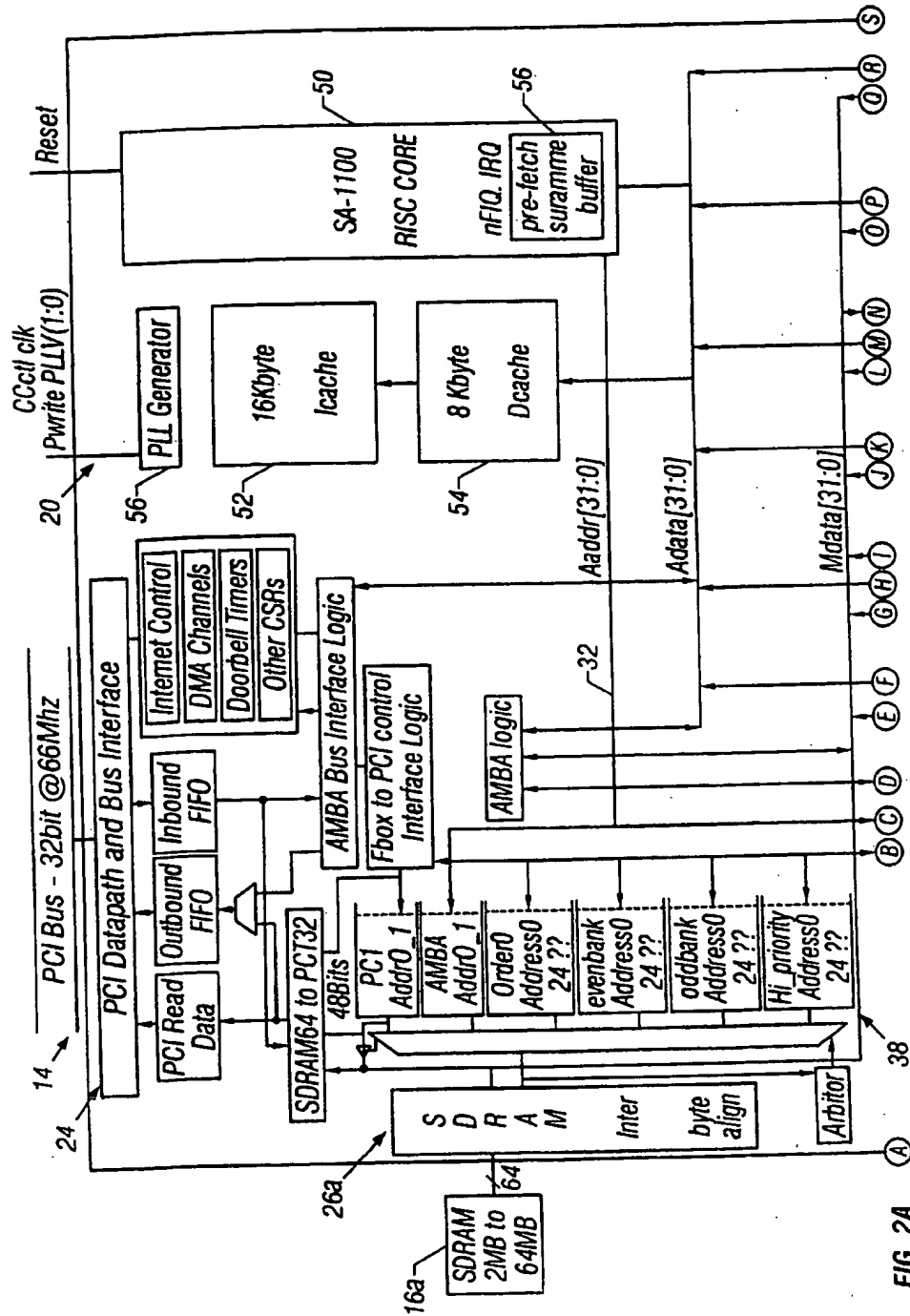


FIG. 2A

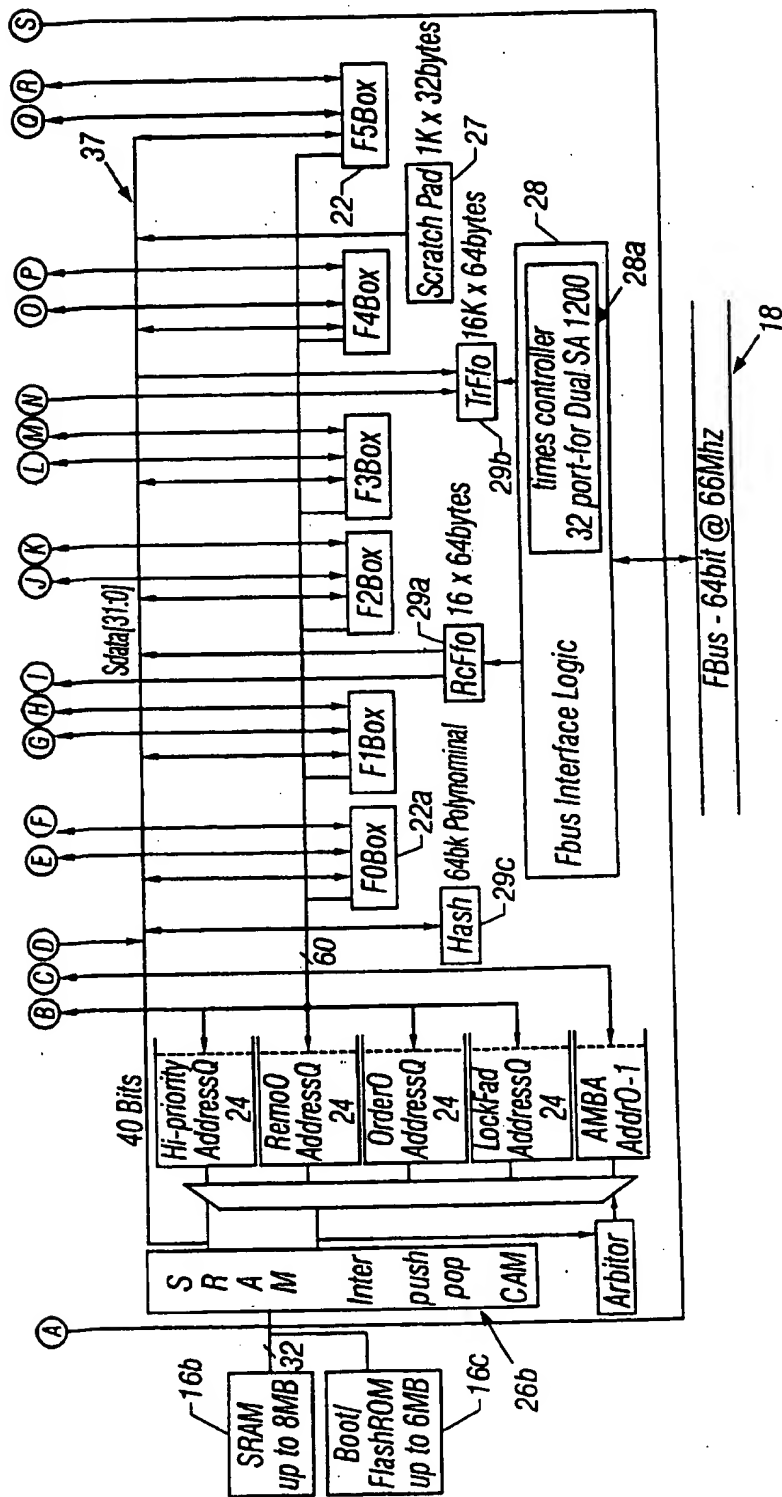


FIG. 2B

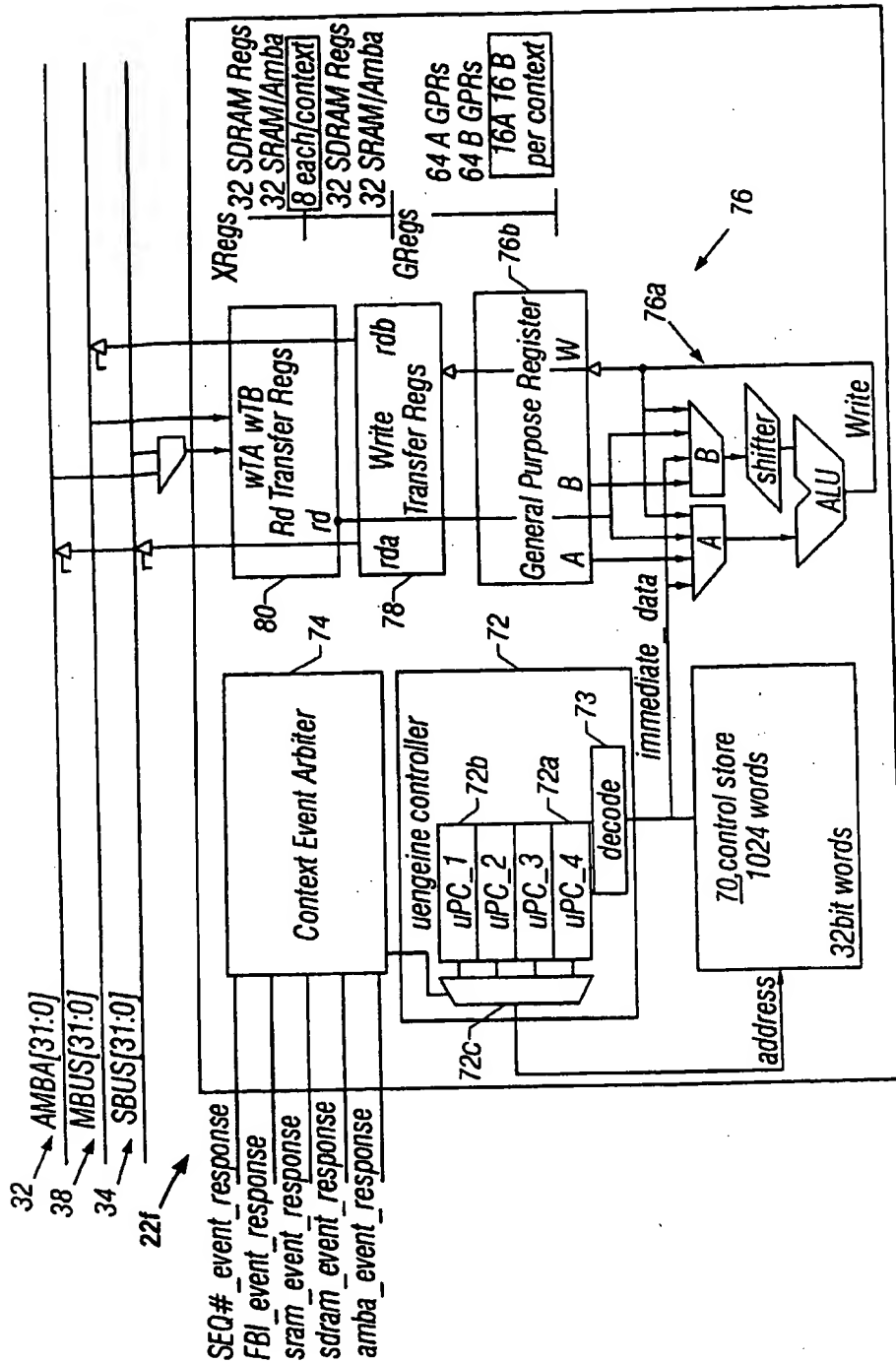
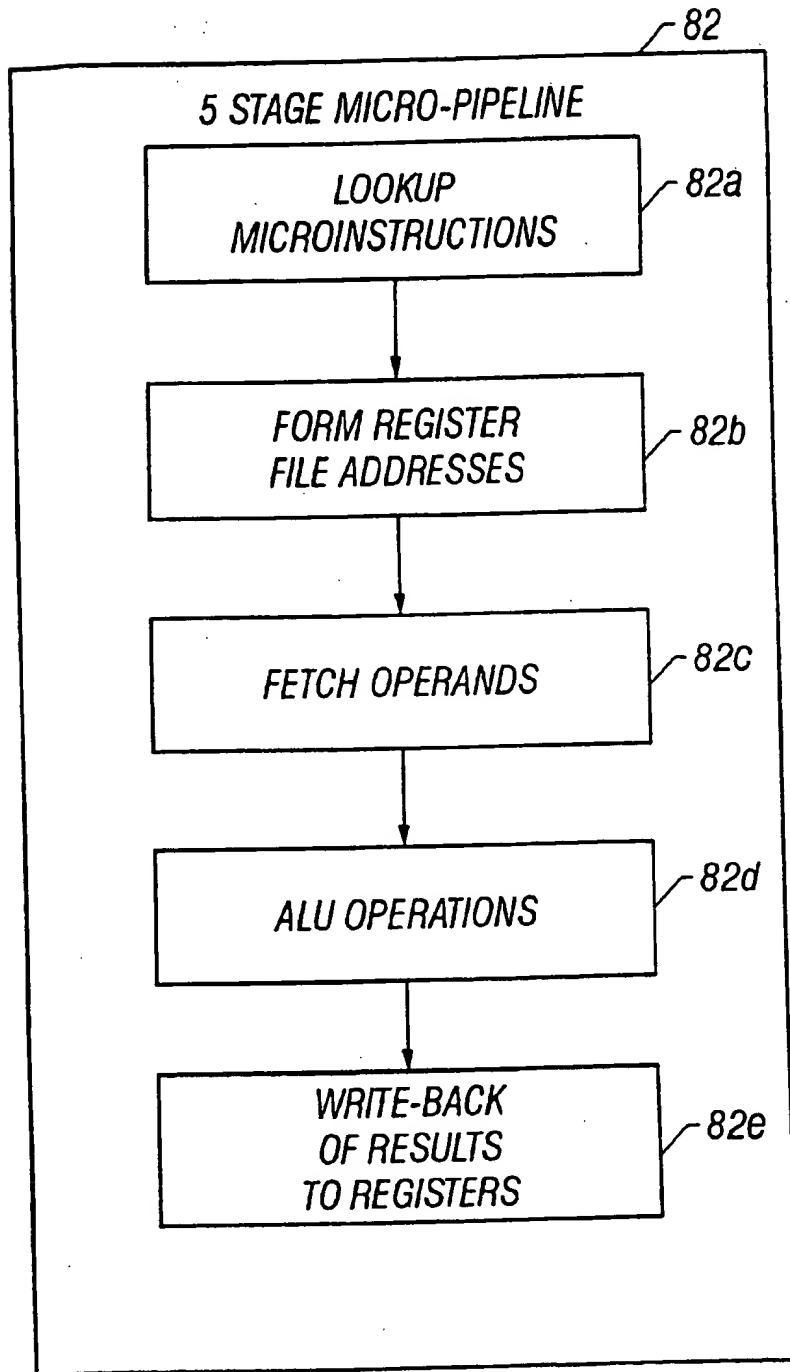
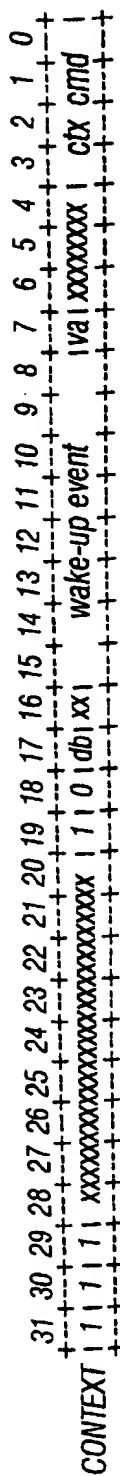


FIG. 3



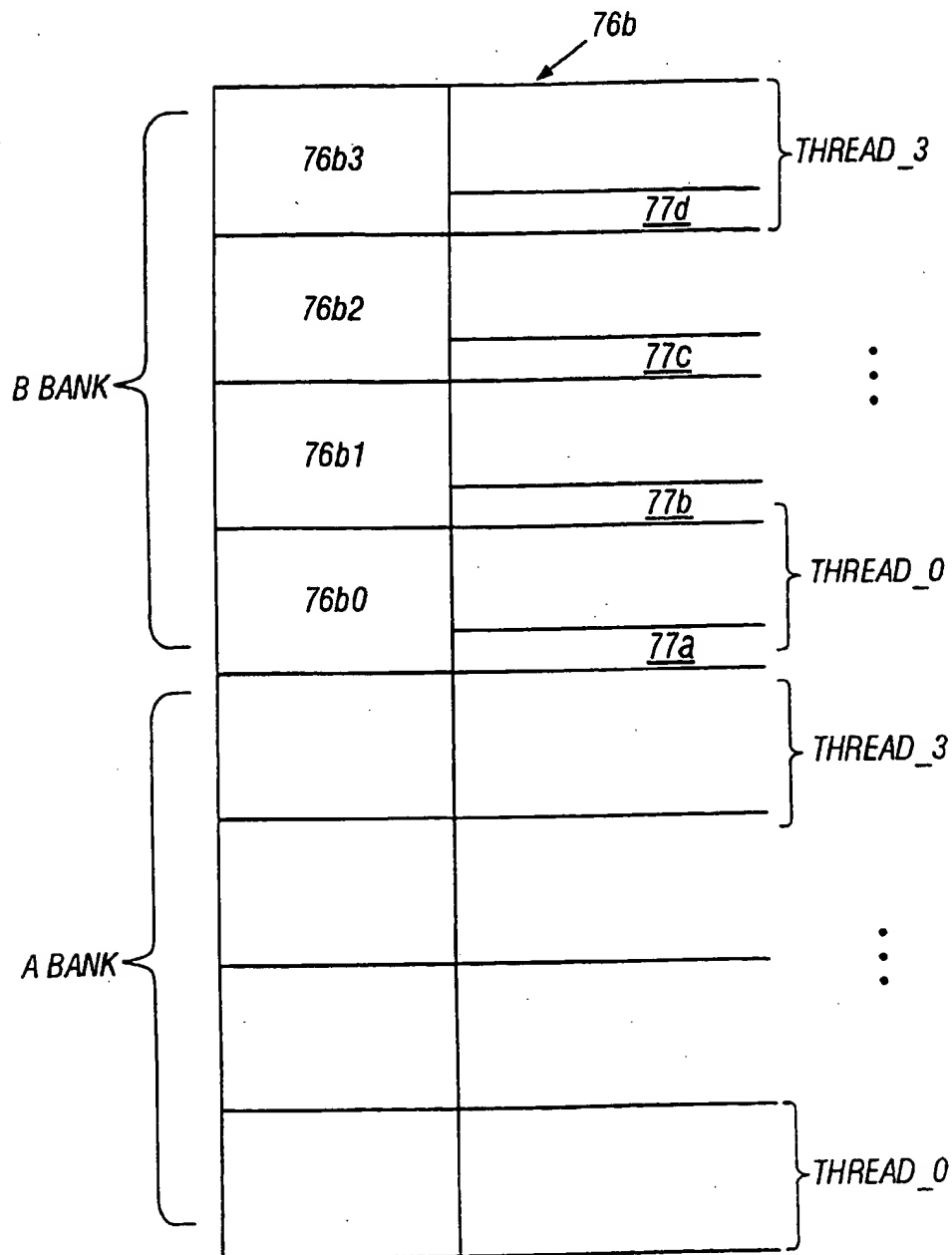
**FIG. 3A**



**Context Descriptors:**

- 0 = kill
  - 1 = voluntary
  - 2 = SRAM
  - 4 = SDRAM
  - 8 = FBI
  - 16 = INTER\_THREAD
  - 32 = PCI\_DMA\_1
  - 64 = PCI\_DMA\_2
  - 128 = SEQ\_NUM\_LSB
- 2) db -> branch defer amount (Bit 17)
- 3) va -> value of sequence number (Bit 7)
- 4) OPCODE Bits (29-31)
- 5) ext\_cmd

**FIG. 3B**



**FIG. 3C**

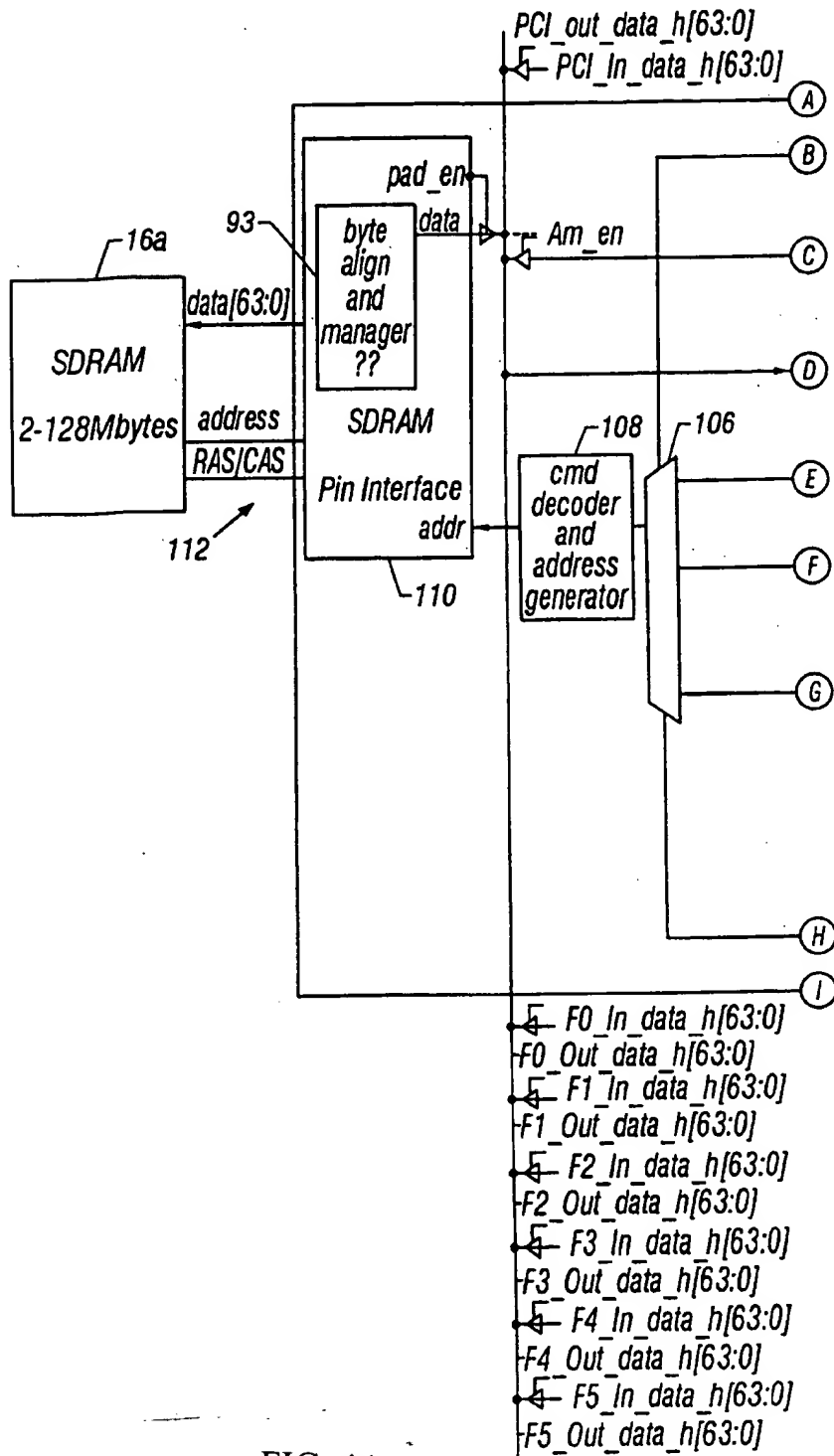


FIG. 4A



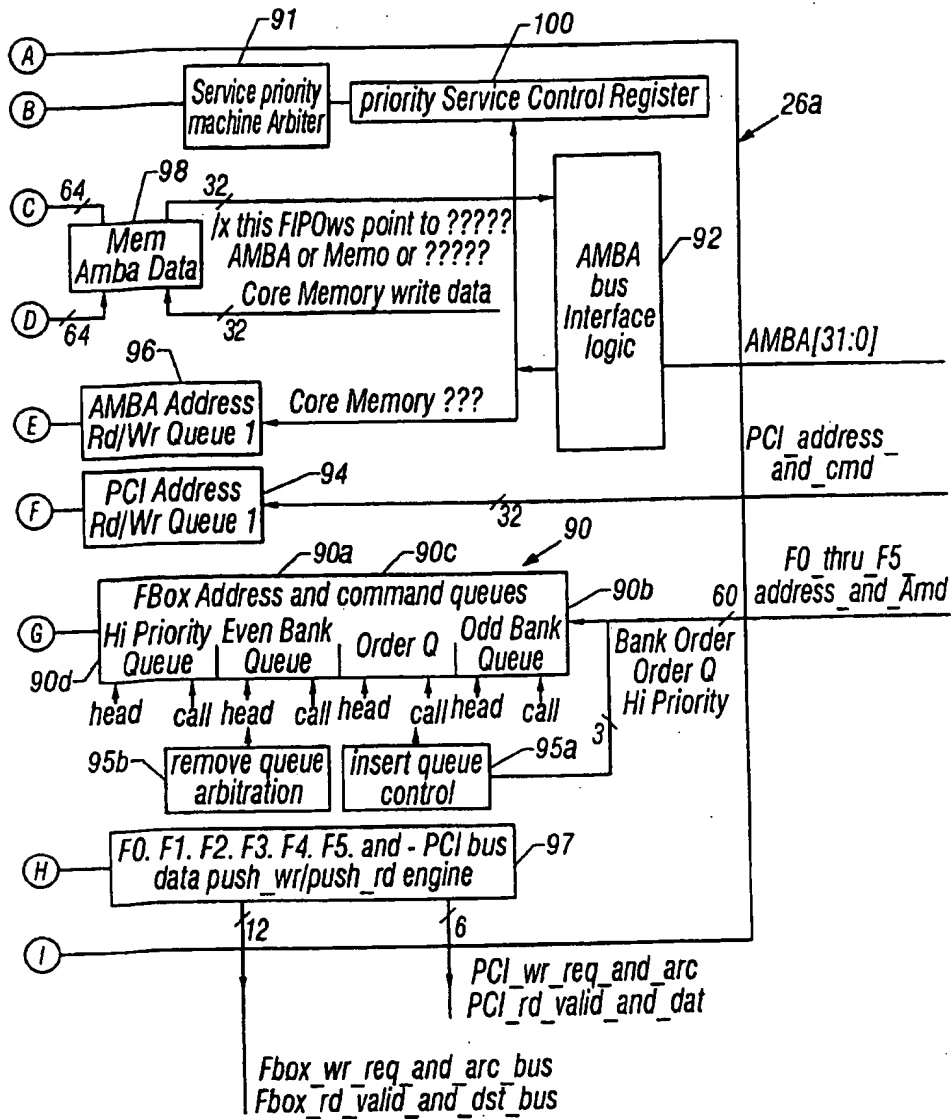


FIG. 4B

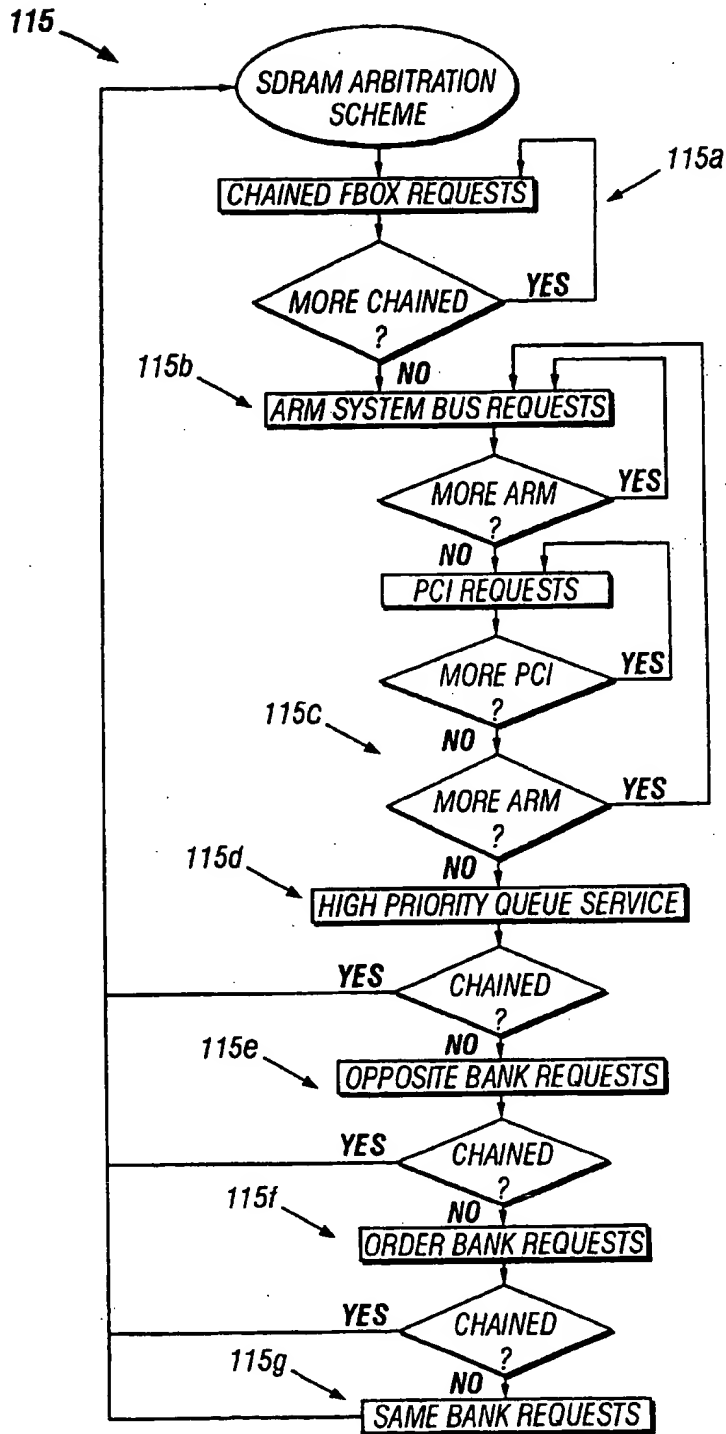


FIG. 4C

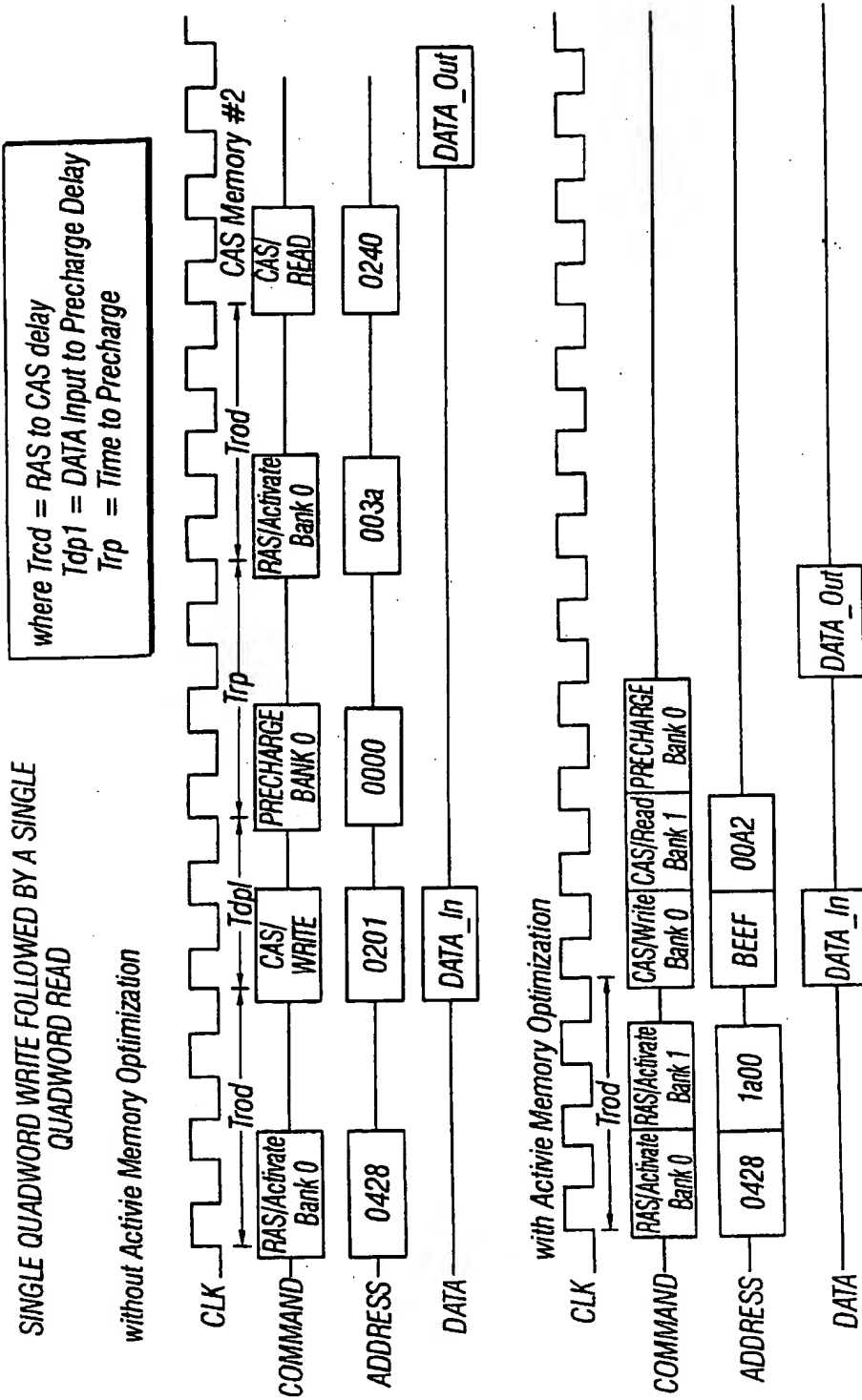


FIG. 4D

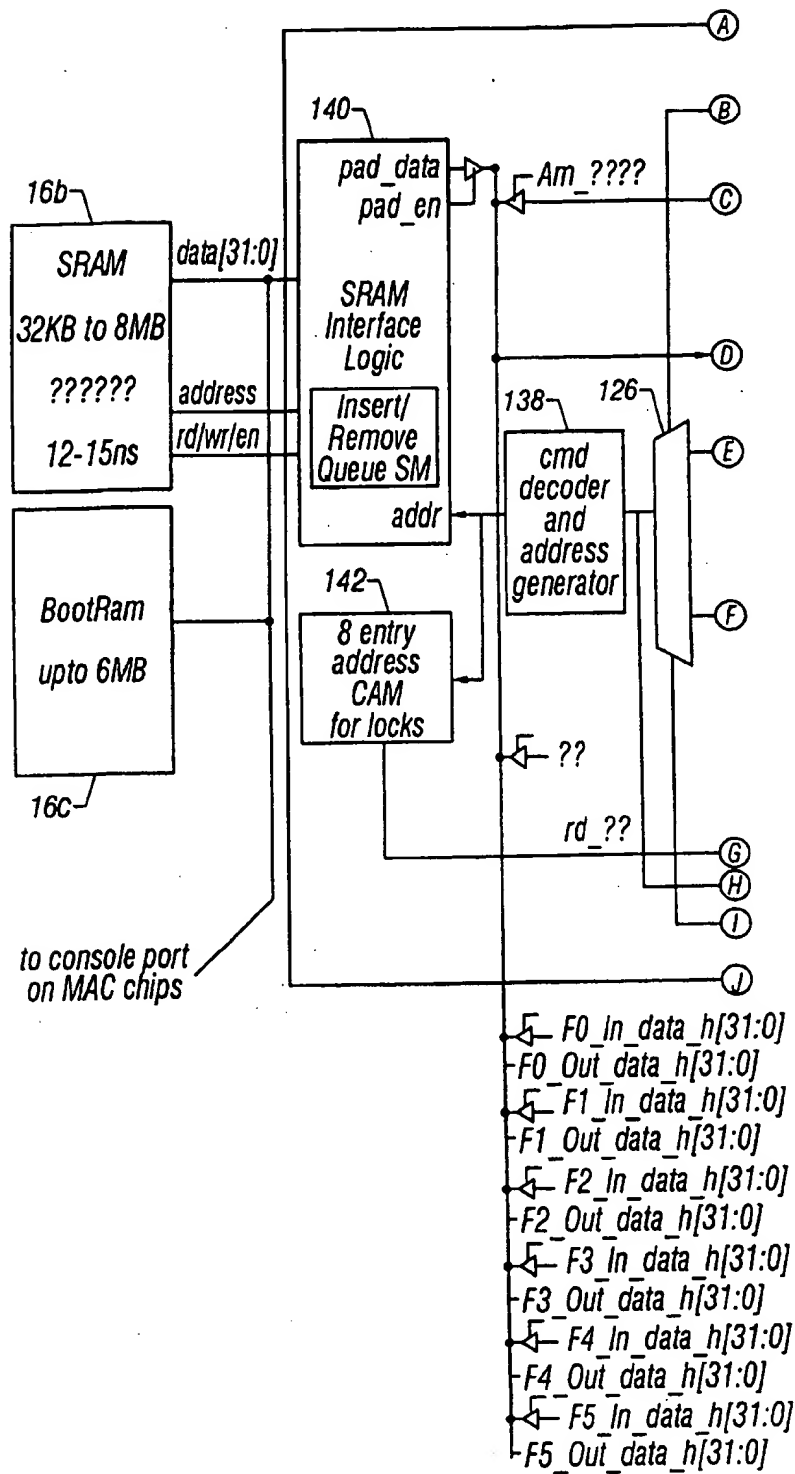


FIG. 5A

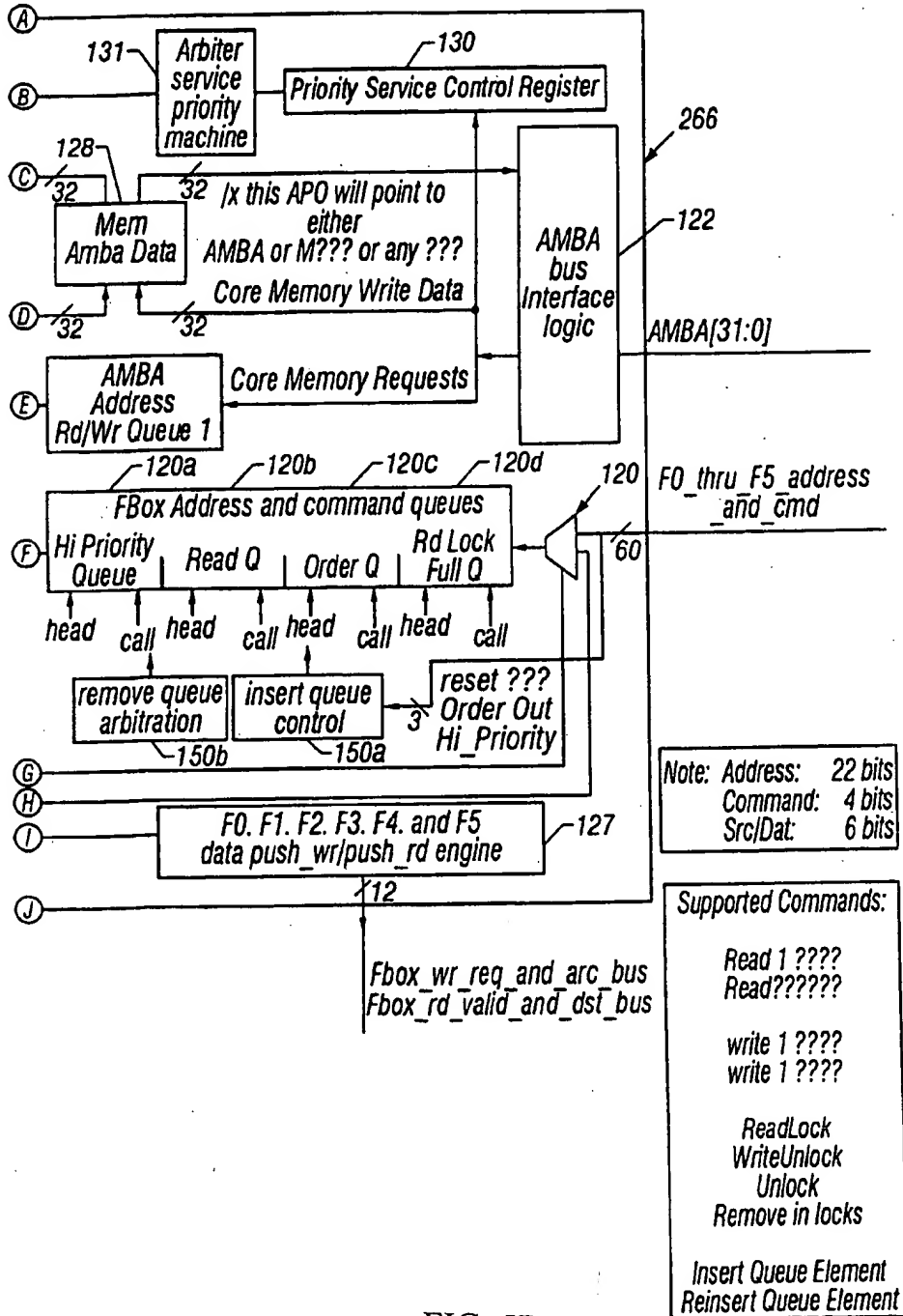
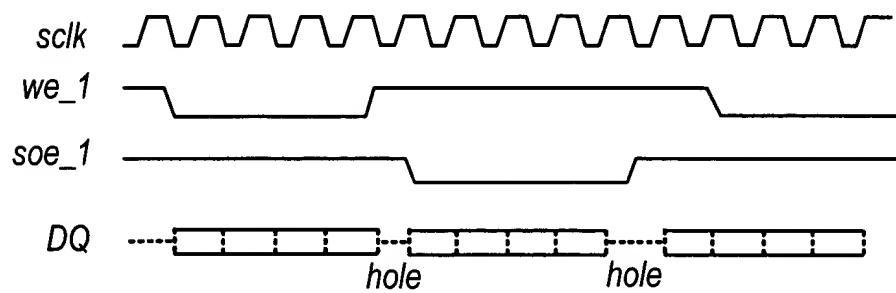


FIG. 5B

*4 Writes and 4 Reads followed by more reads with optimization*



*4 Writes and 4 Reads without optimization*

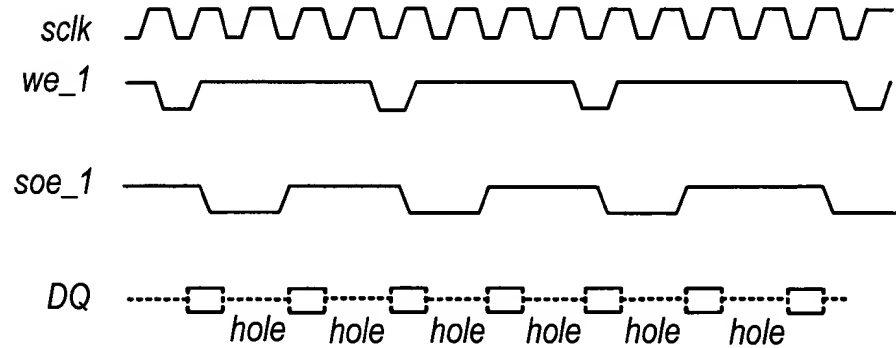


FIG. 5C

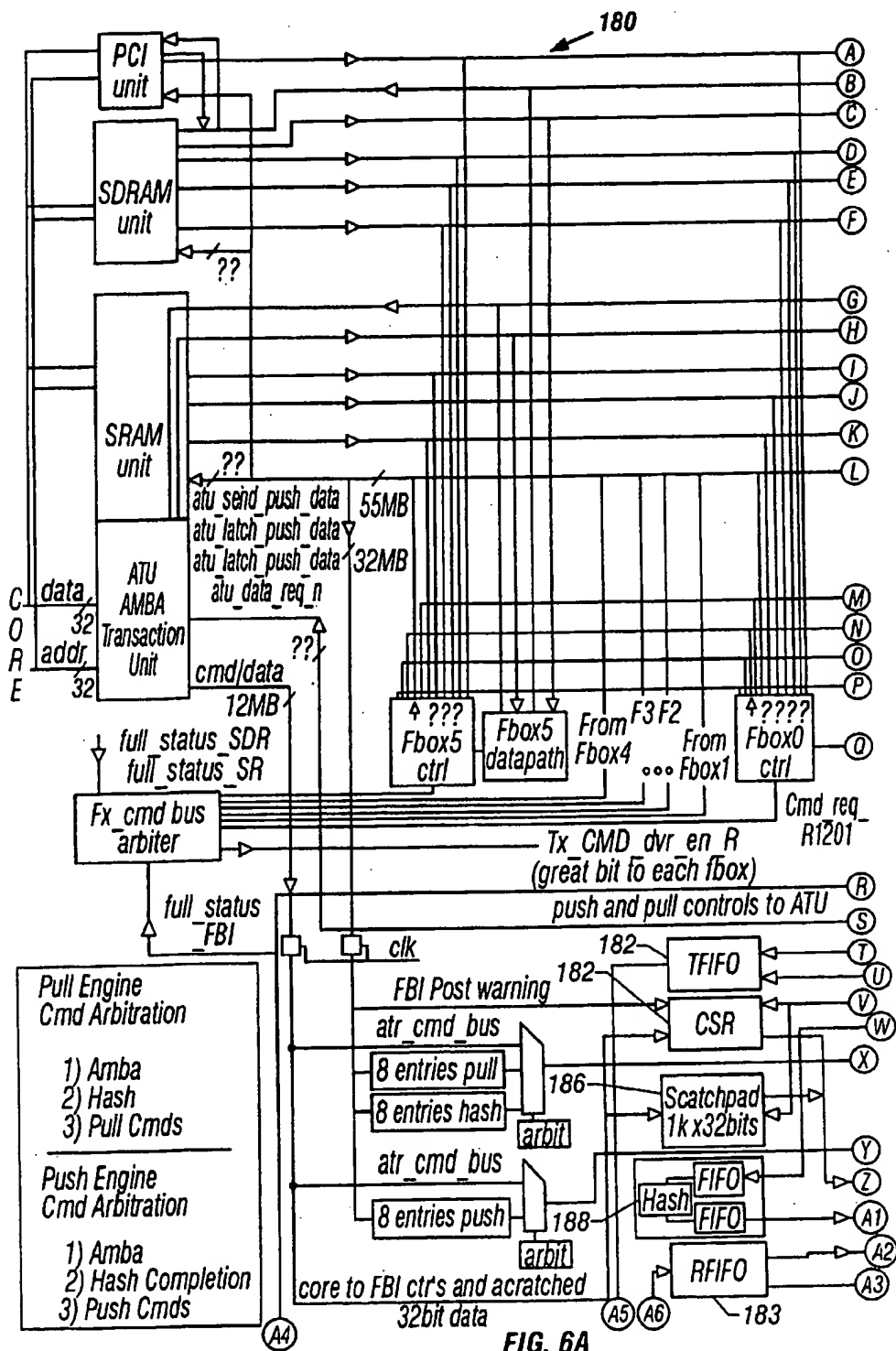
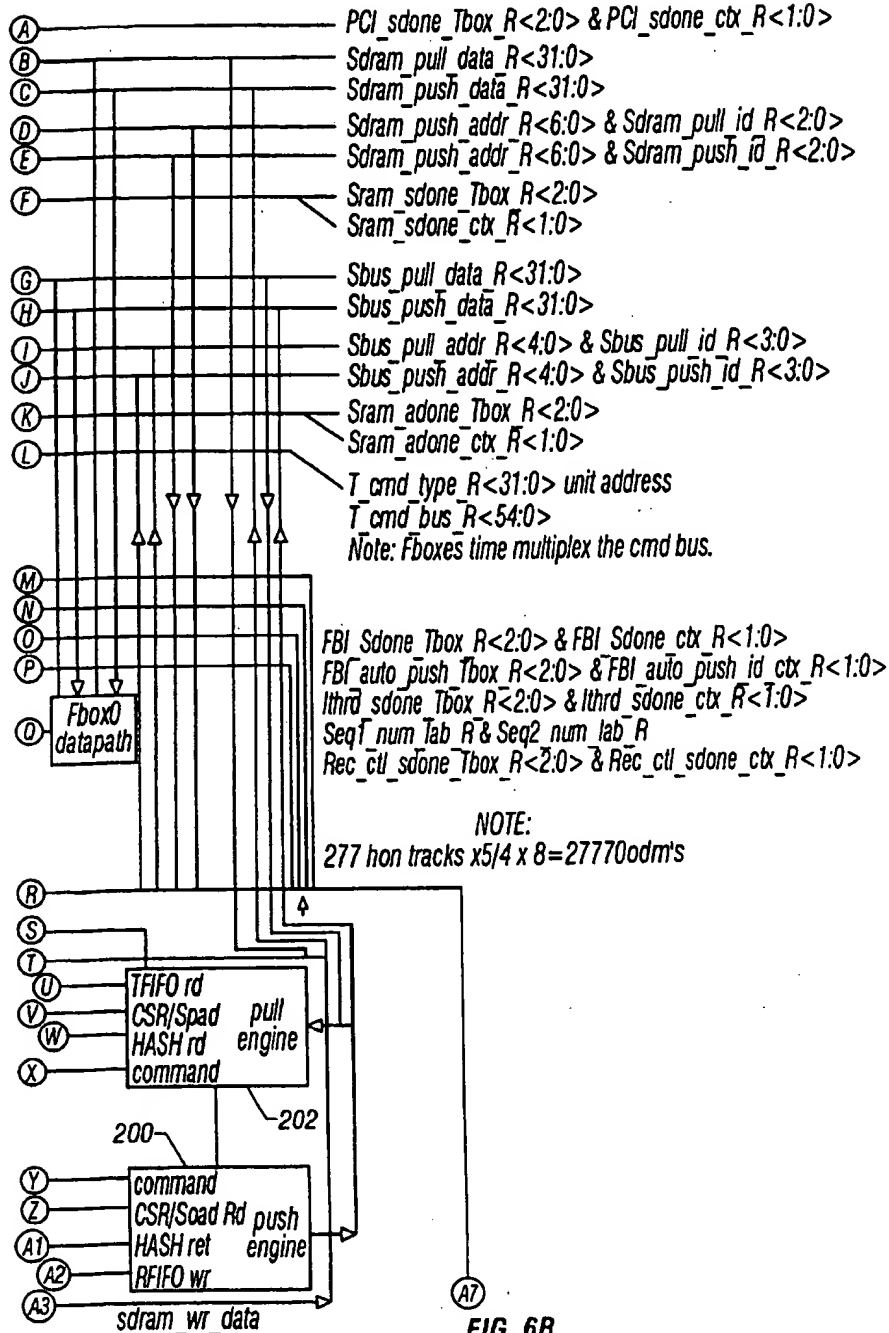


FIG. 6A





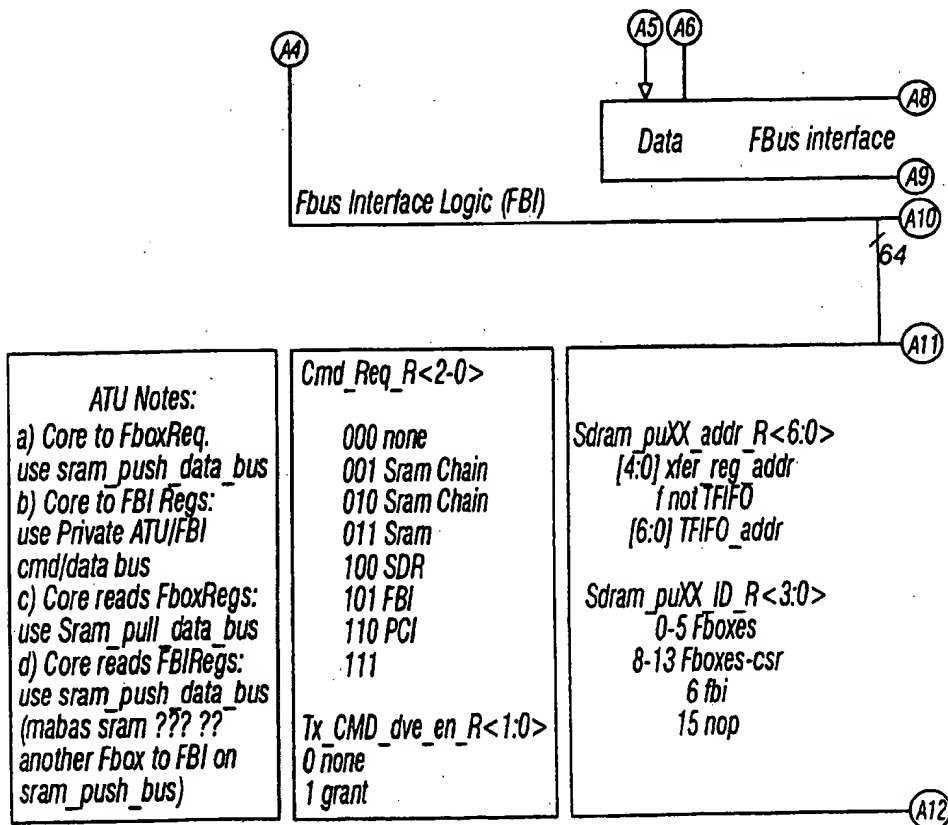


FIG. 6C

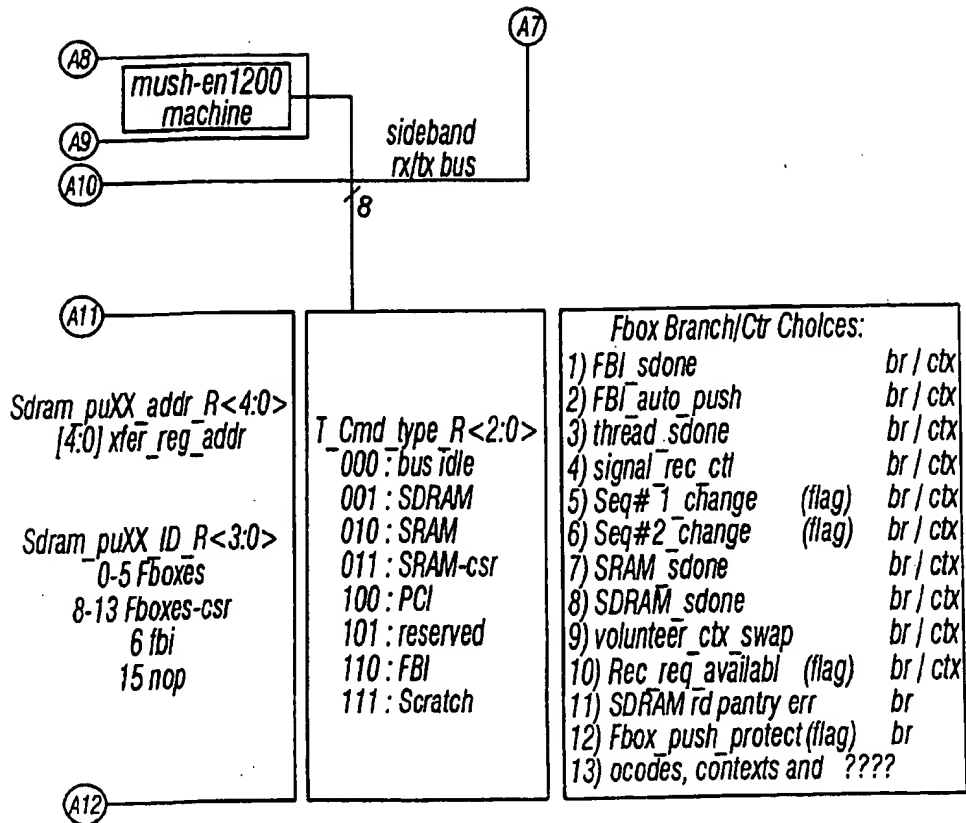


FIG. 6D